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Applicant : Gilbert Wolrich et al.
Serial No. : 10/070,091
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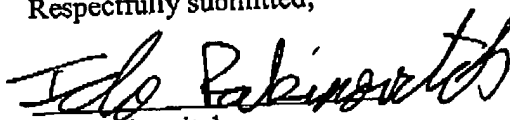
Title : REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR
ARCHITECTURE

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is **AMENDMENT IN REPLY
TO FINAL ACTION OF FEBRUARY 7, 2006** and **CHANGE OF CORRESPONDENCE
ADDRESS**, faxed this 6th day of April, 2006, to the United States Patent and Trademark Office.

Respectfully submitted,

Date: April 6, 2006


Ido Rabinovitch
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